

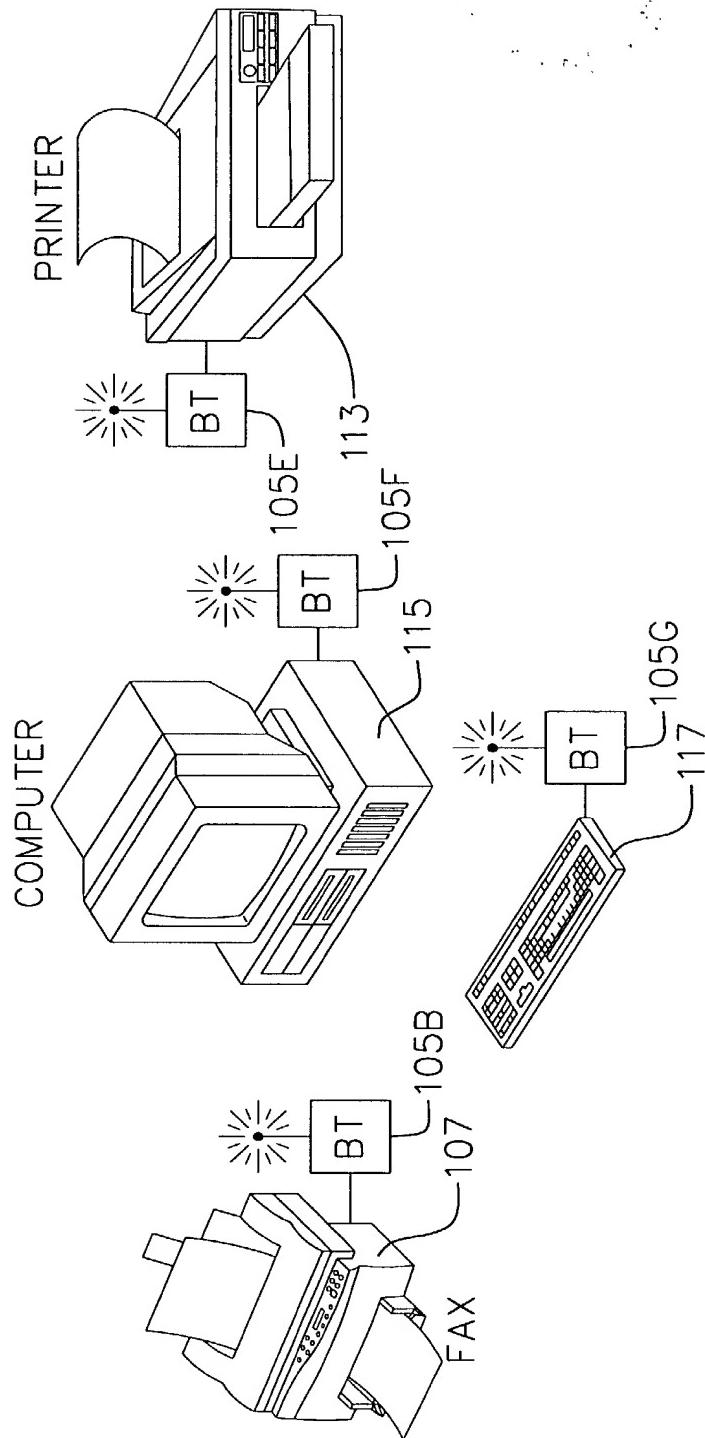
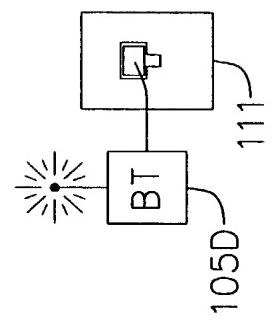
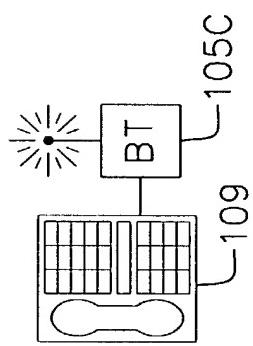
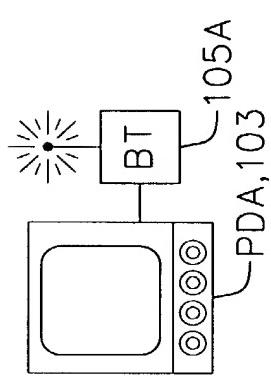
FIG. 1

FIG.2A

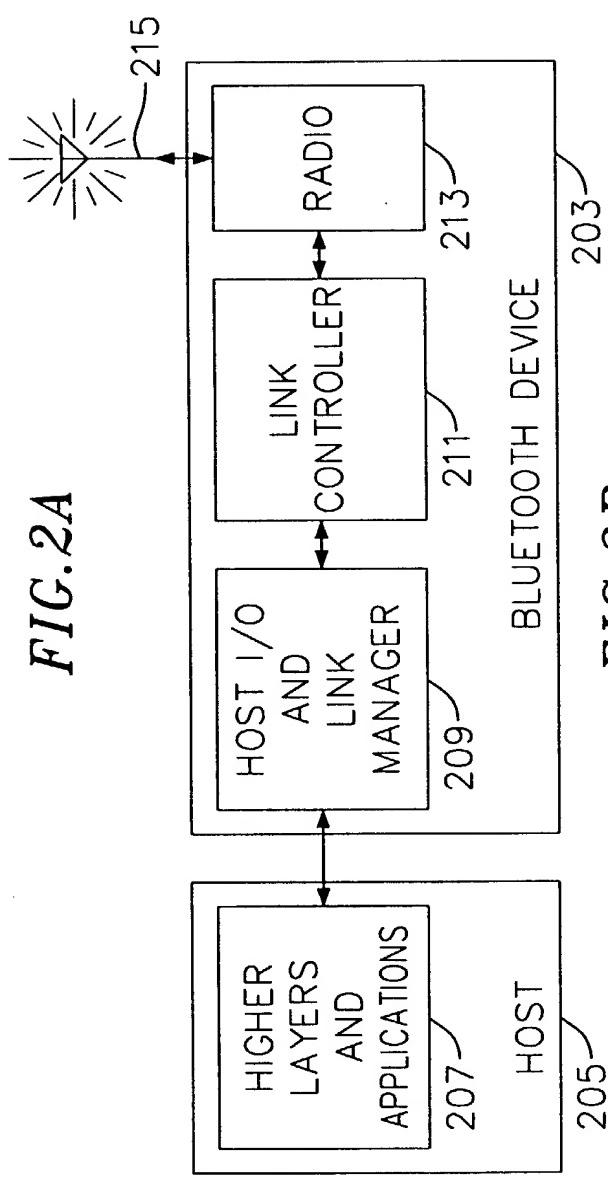


FIG.2B

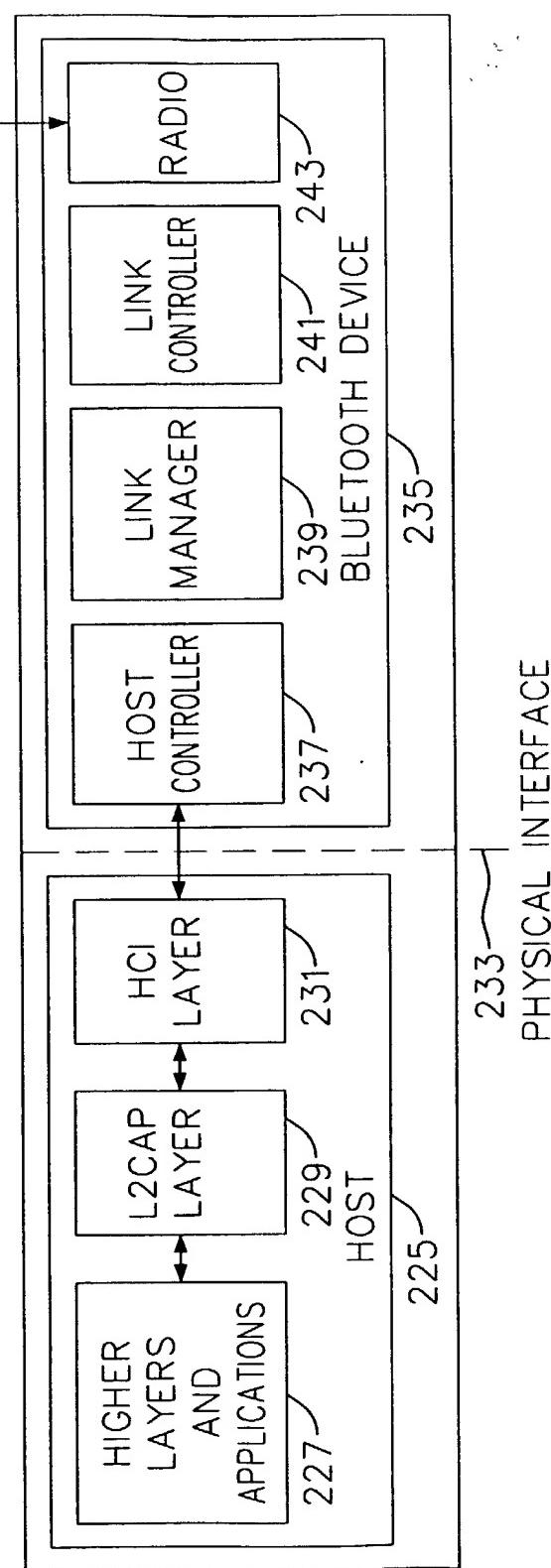


FIG.2C

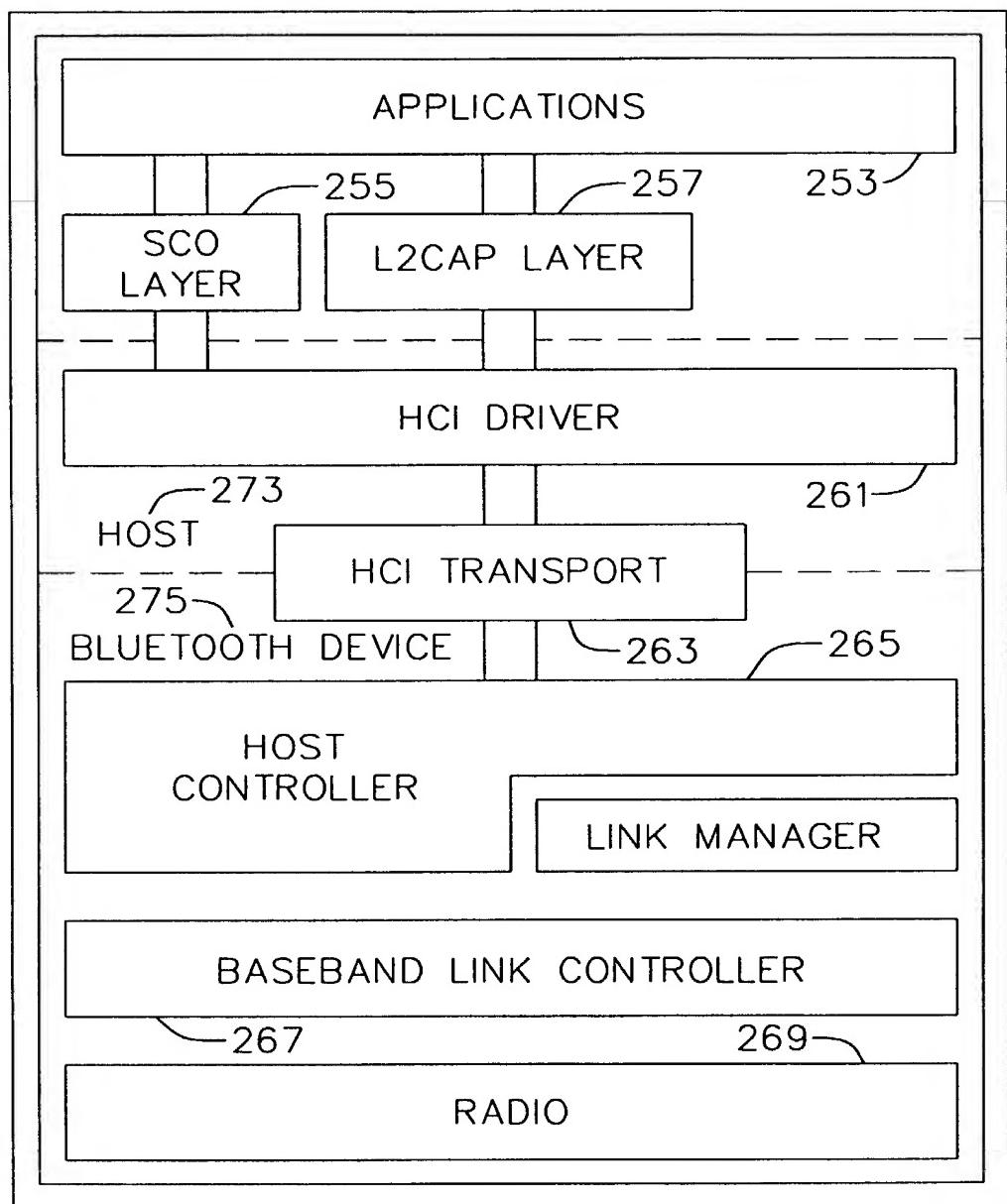


FIG. 3

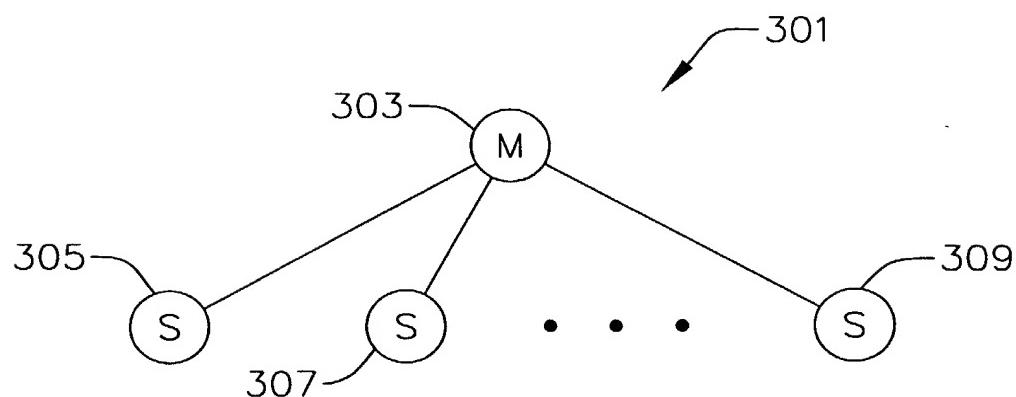


FIG. 4

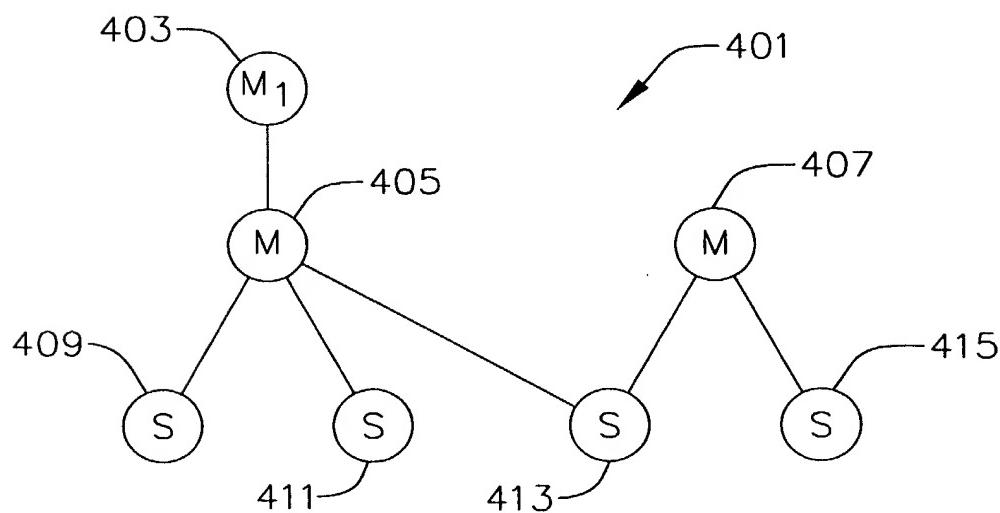


FIG. 5

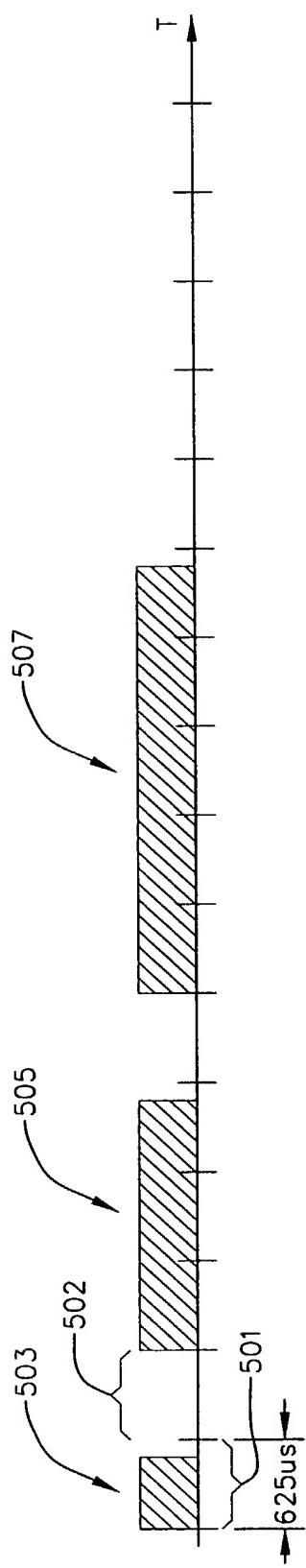


FIG. 6

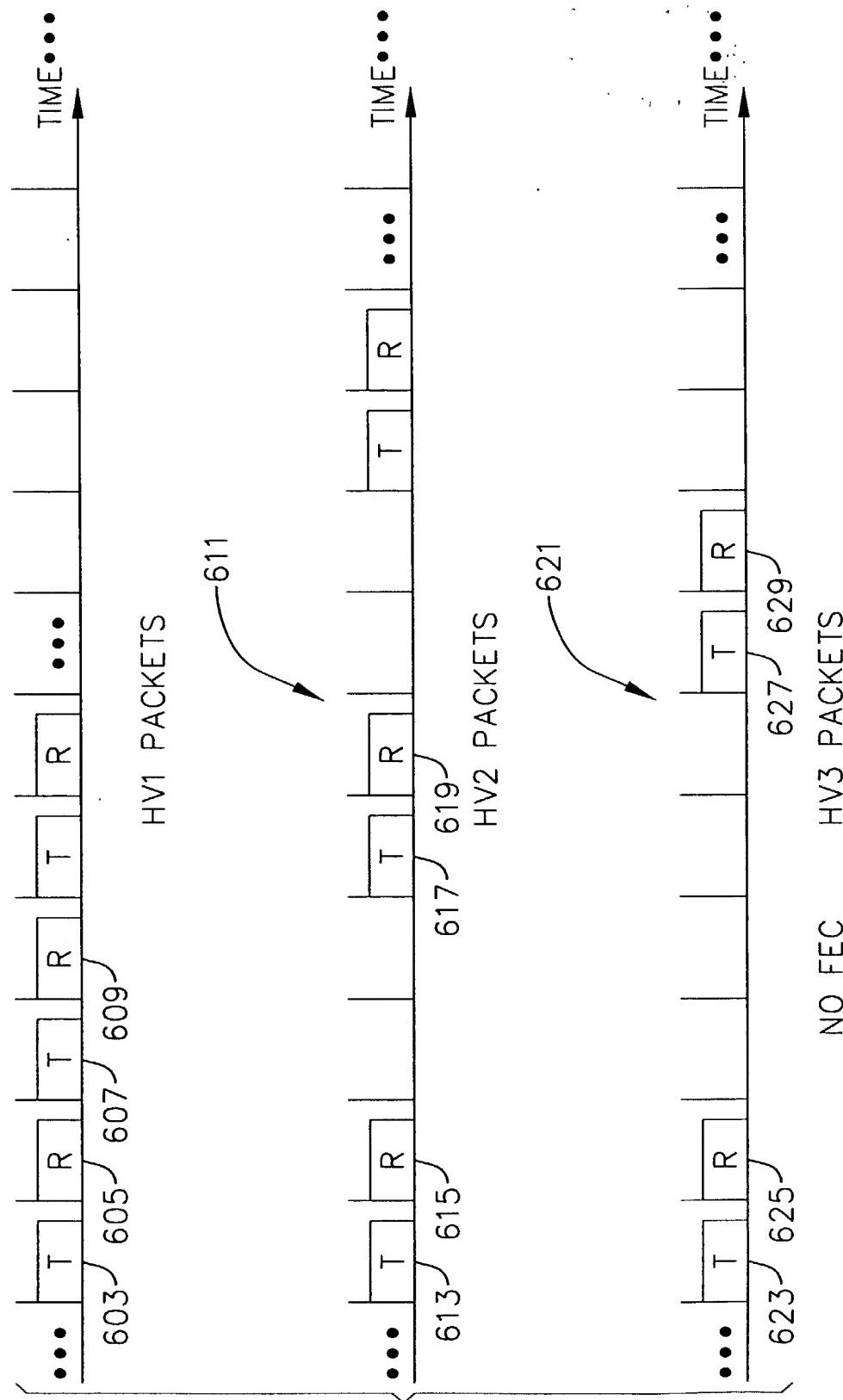


FIG. 7

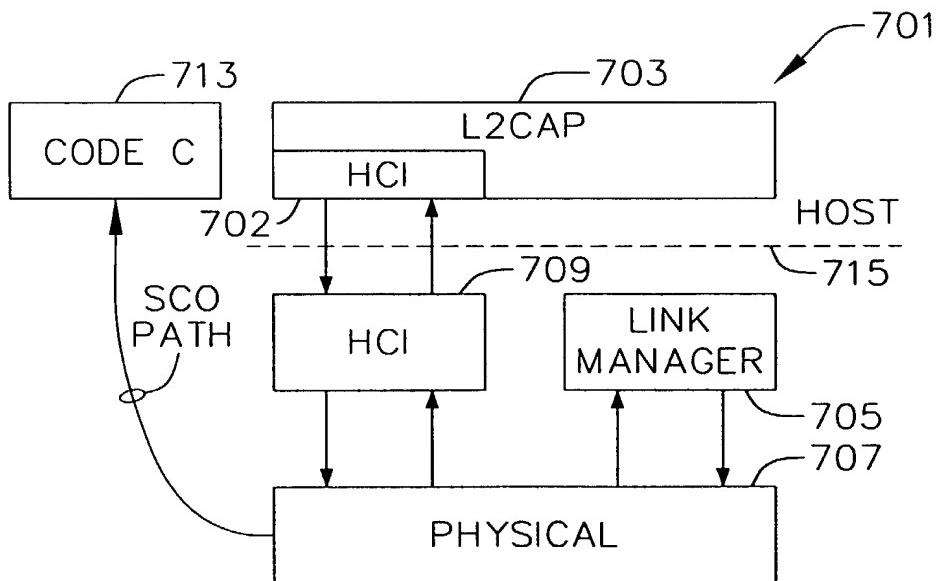


FIG. 8

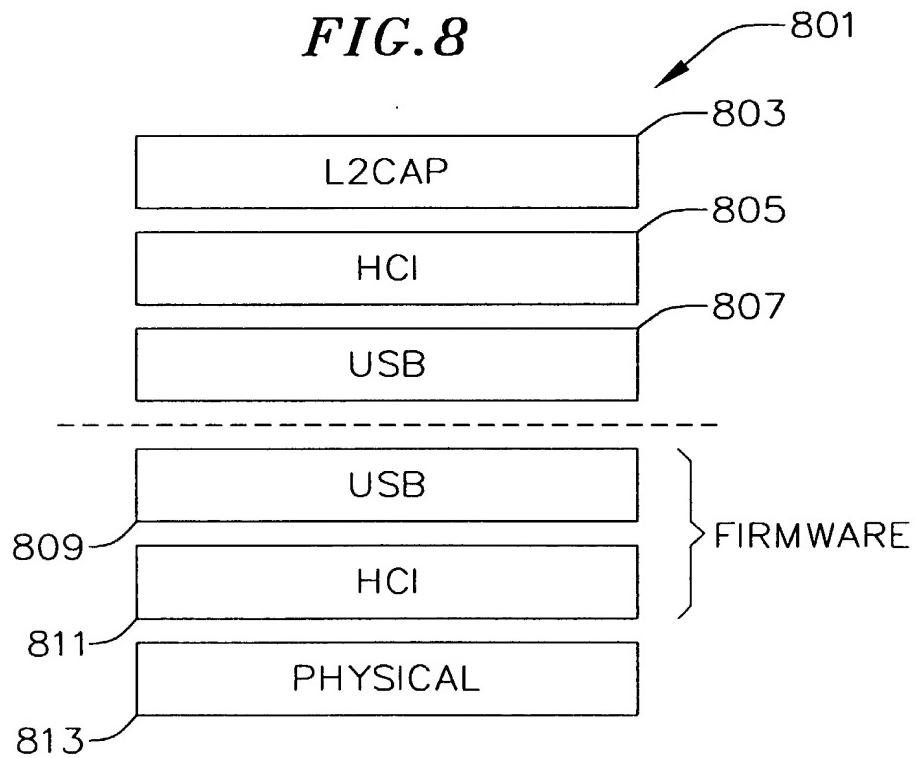


FIG. 9

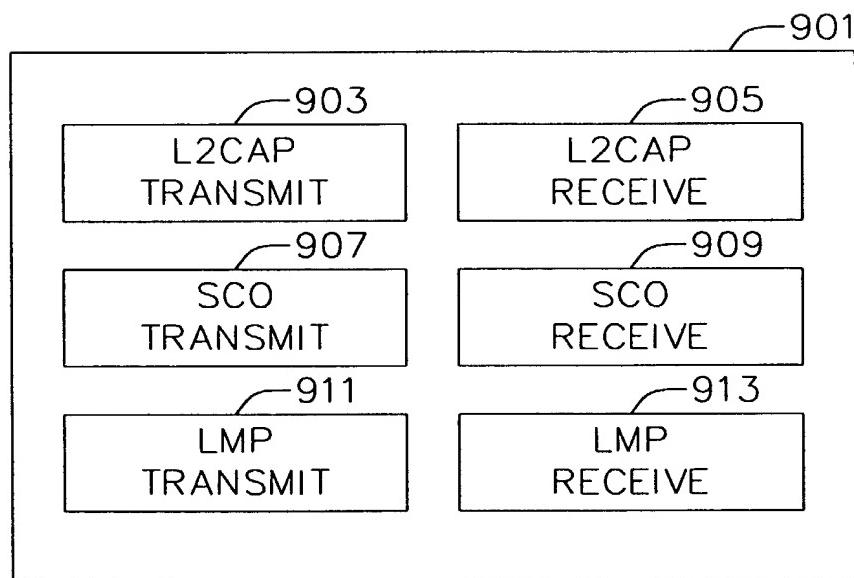


FIG. 10

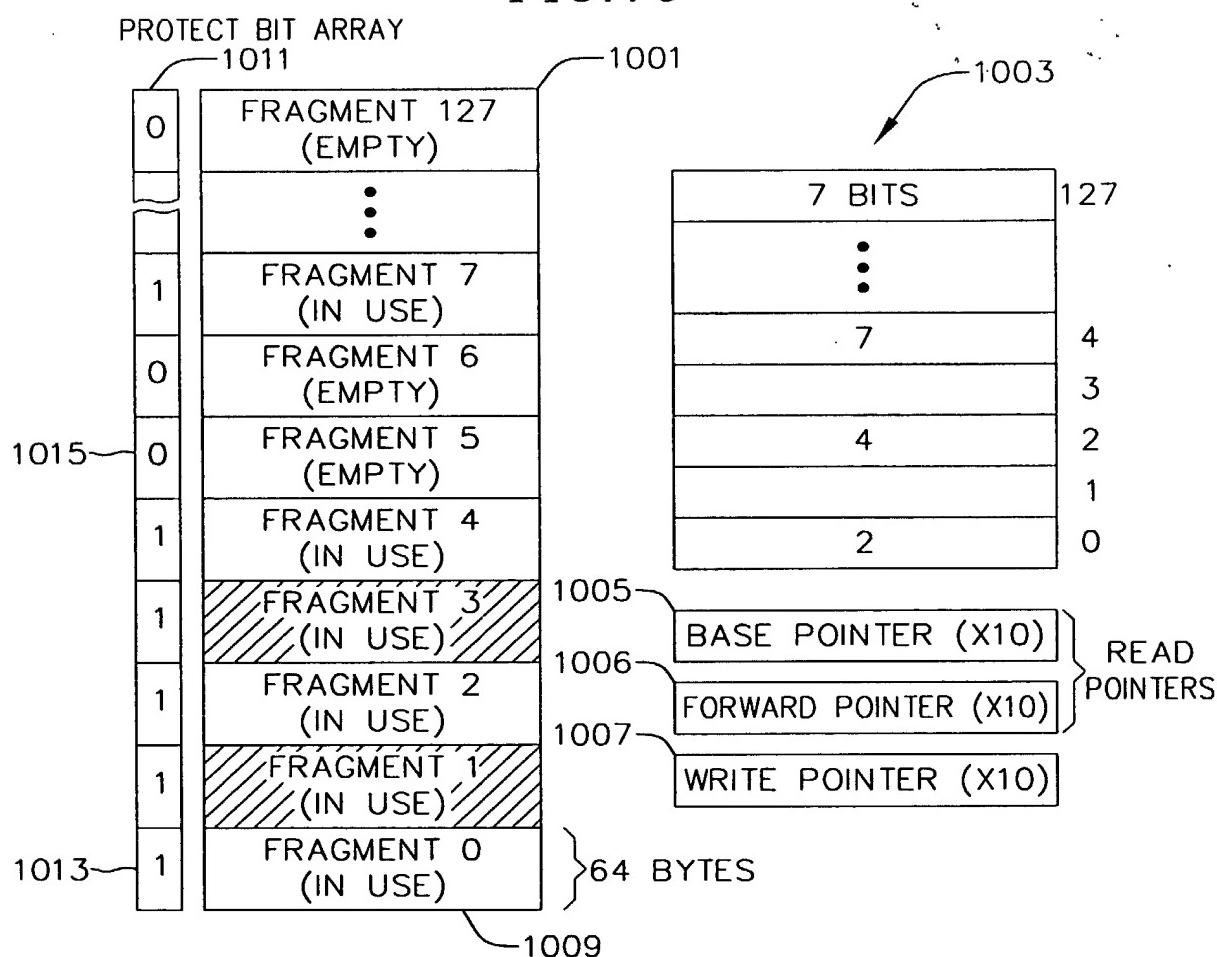


FIG. 11

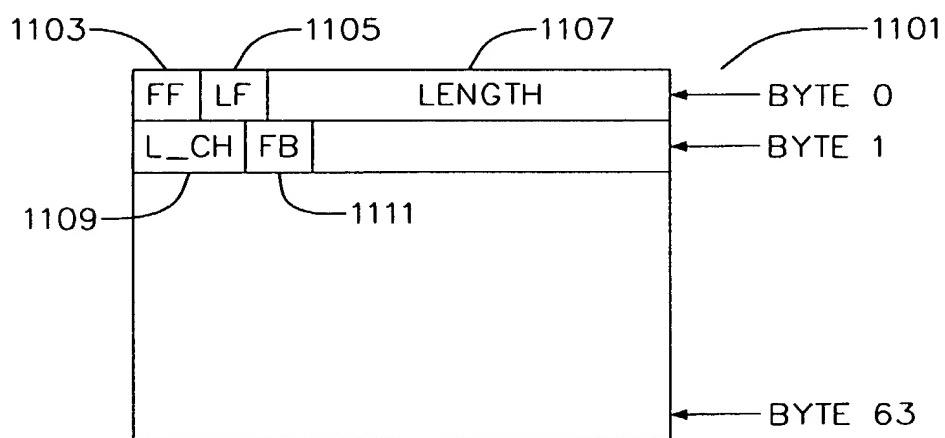


FIG. 12

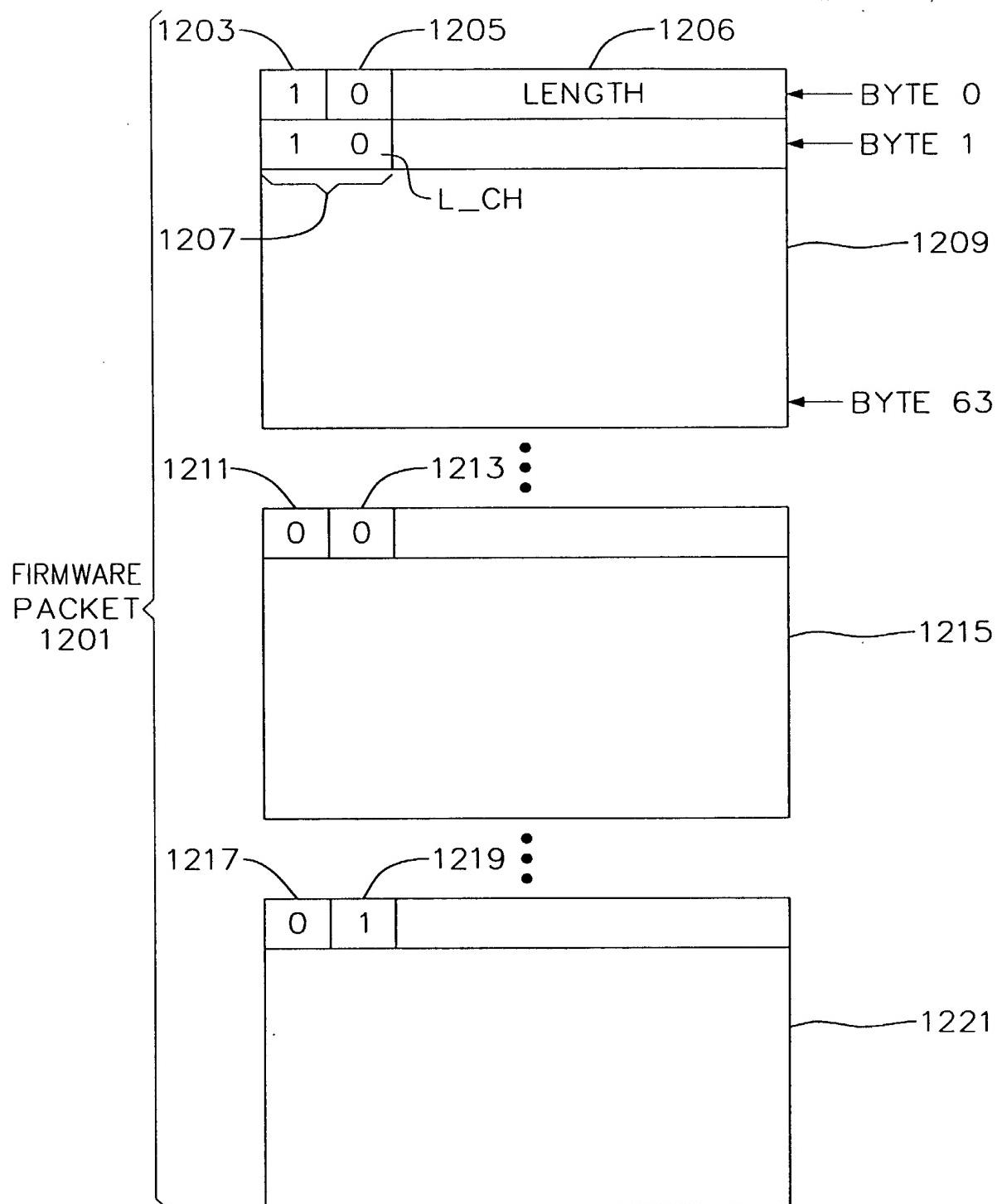


FIG. 13

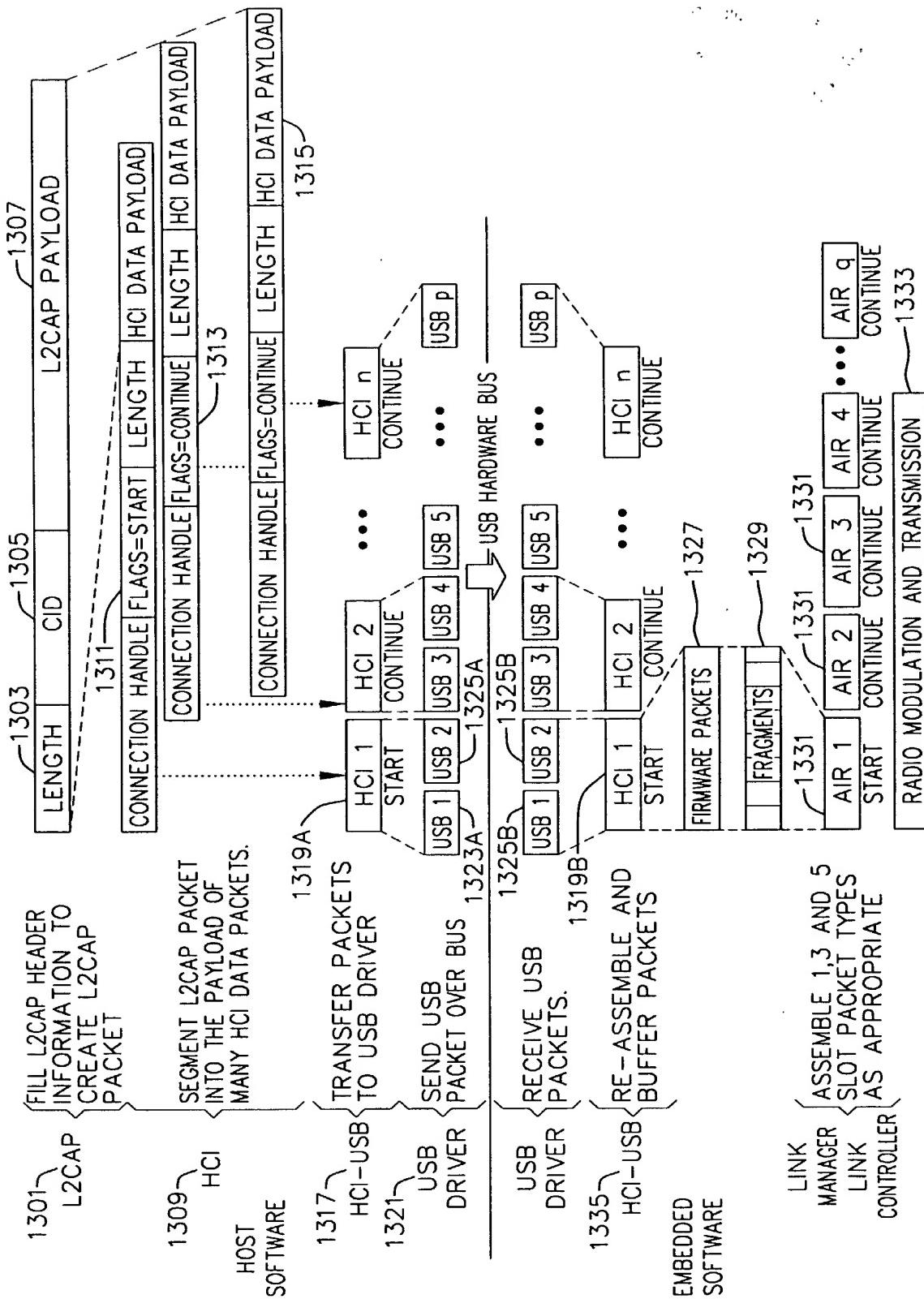


FIG. 14

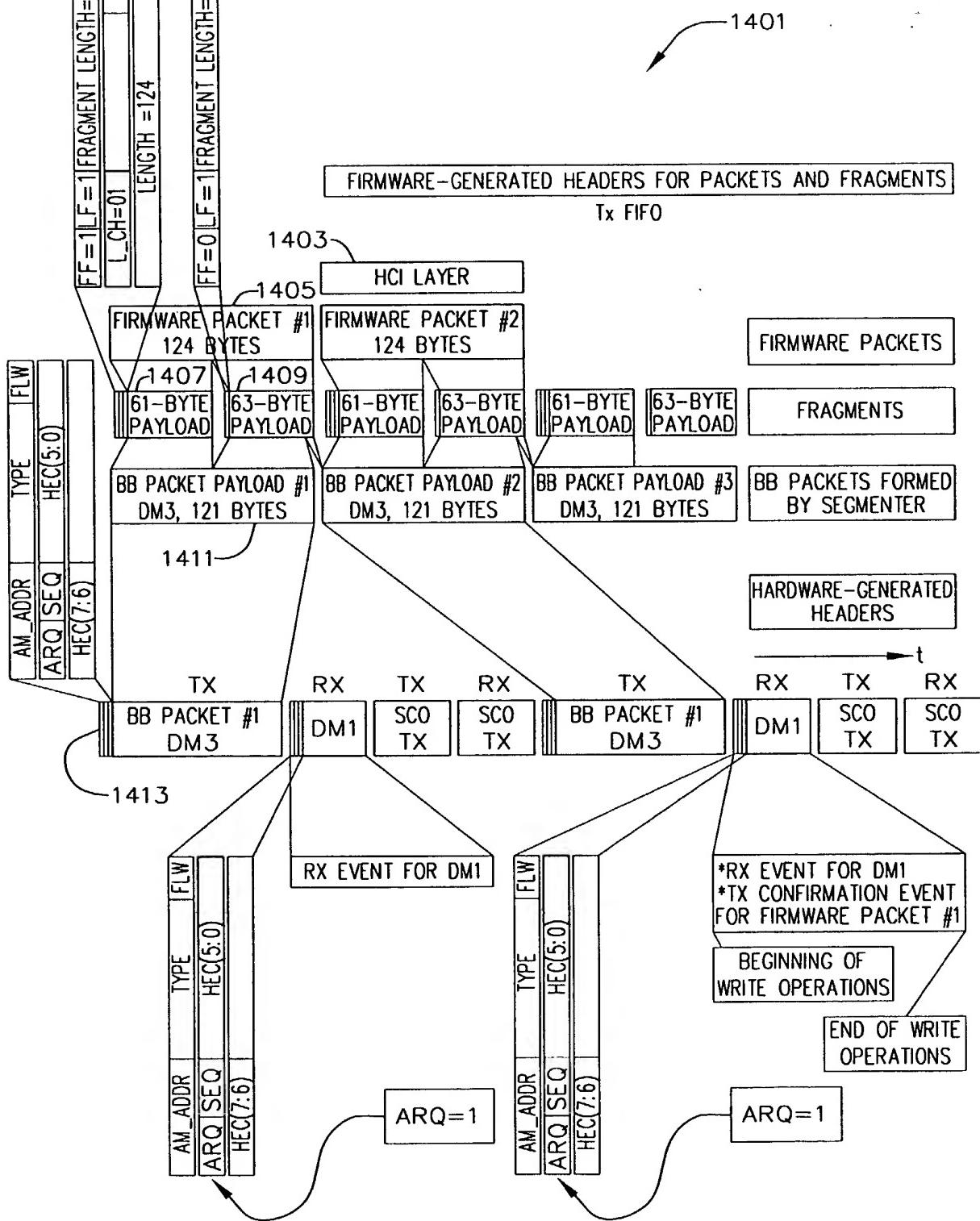


FIG. 15

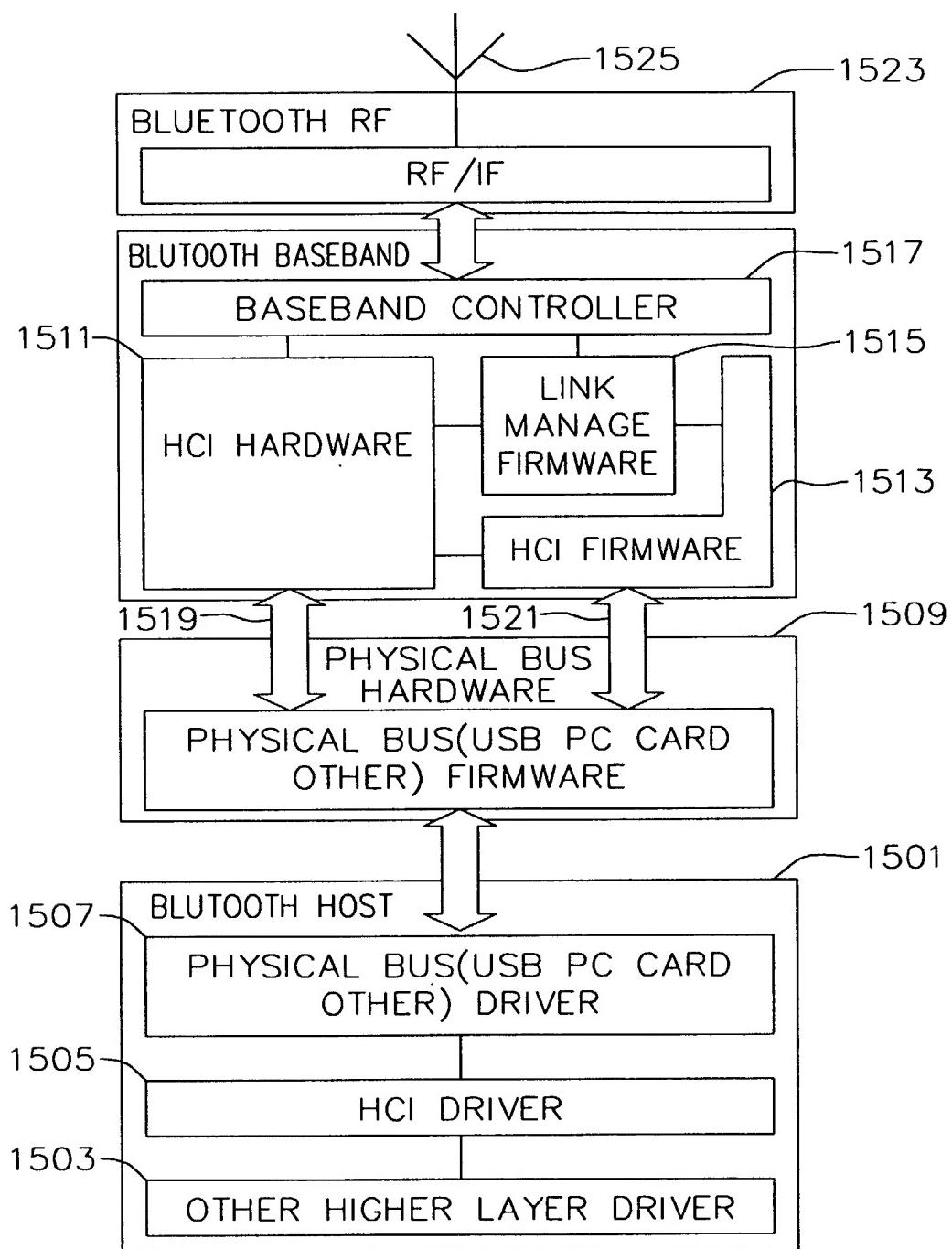


FIG. 16

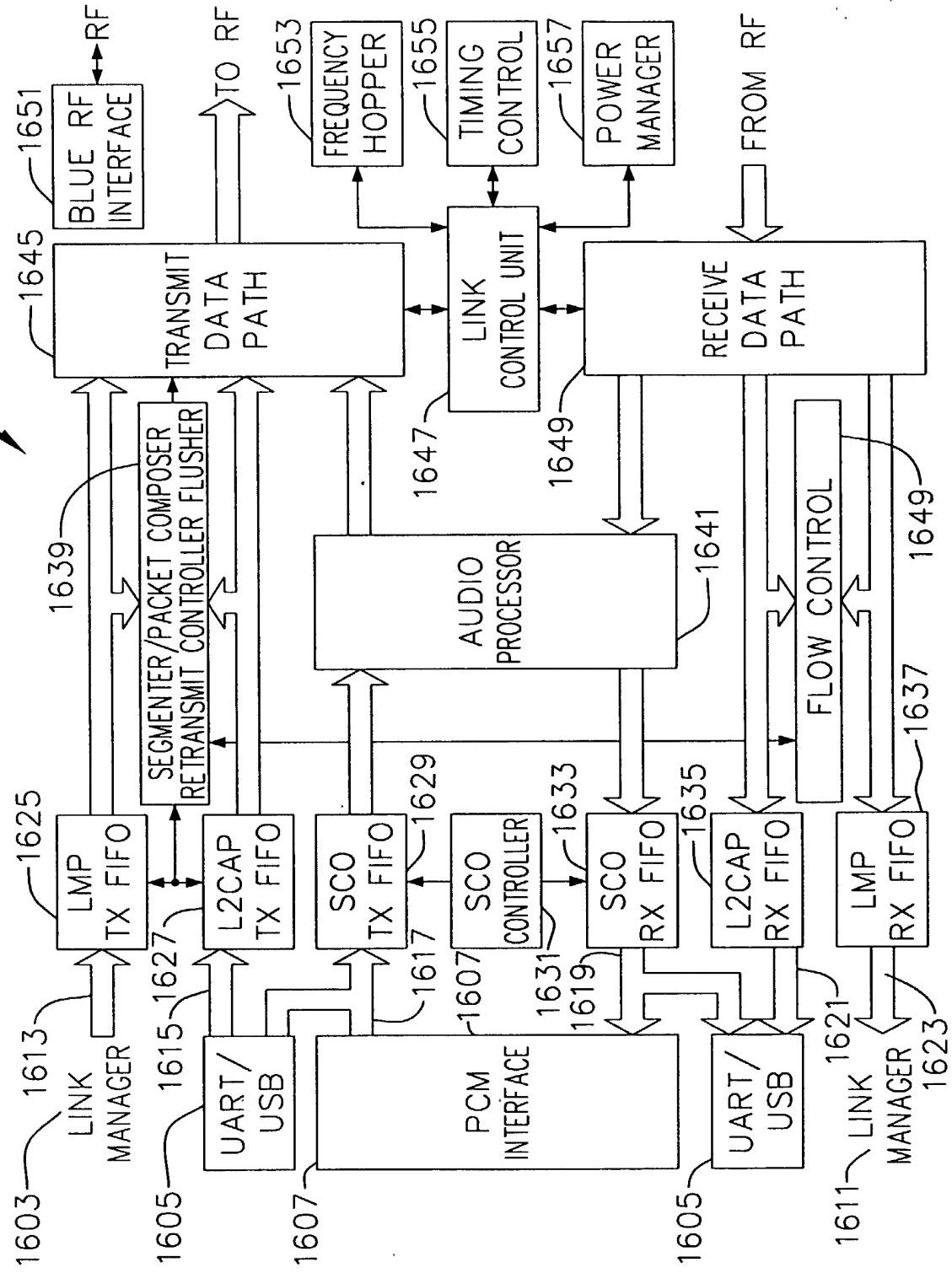


FIG. 17

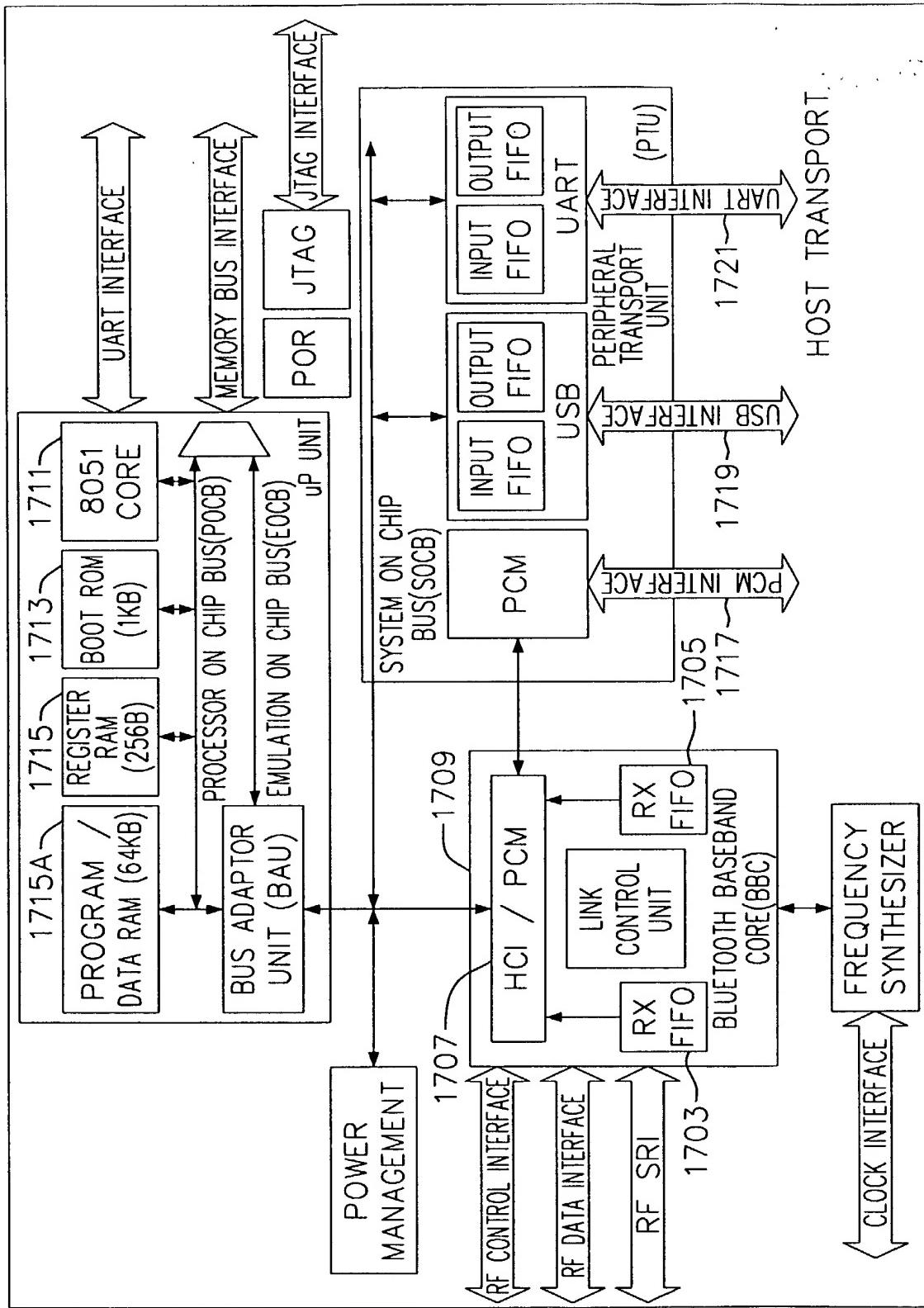


FIG. 18

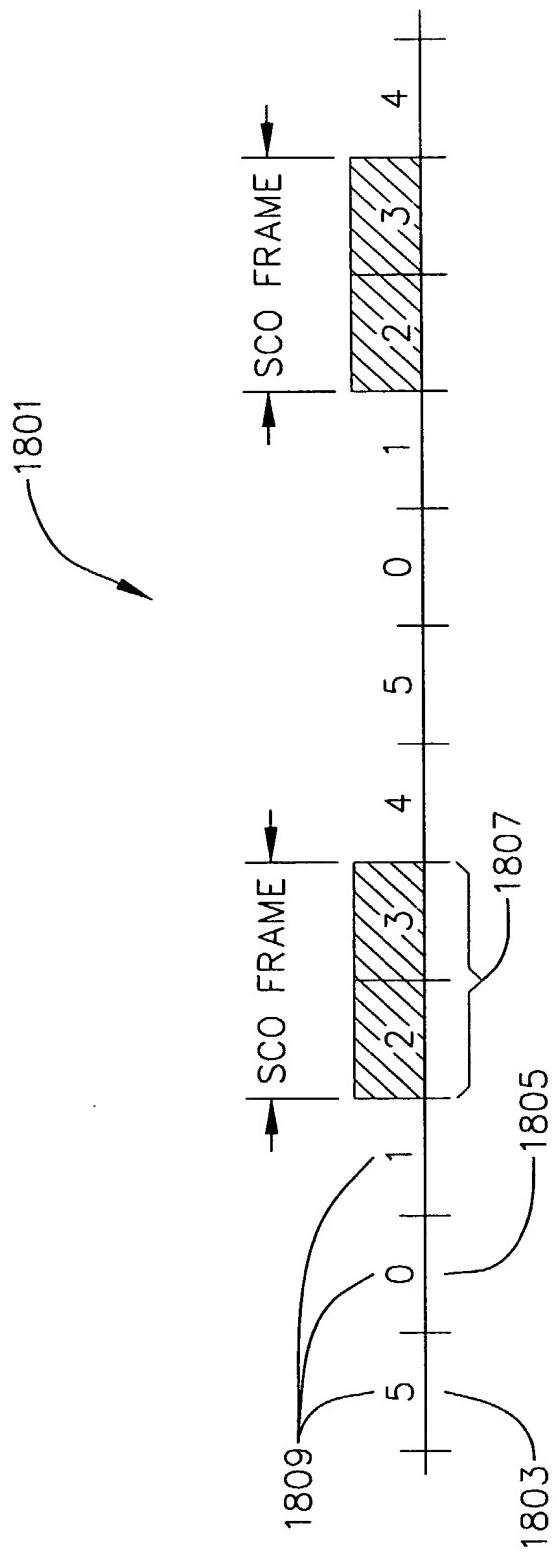


FIG. 19A

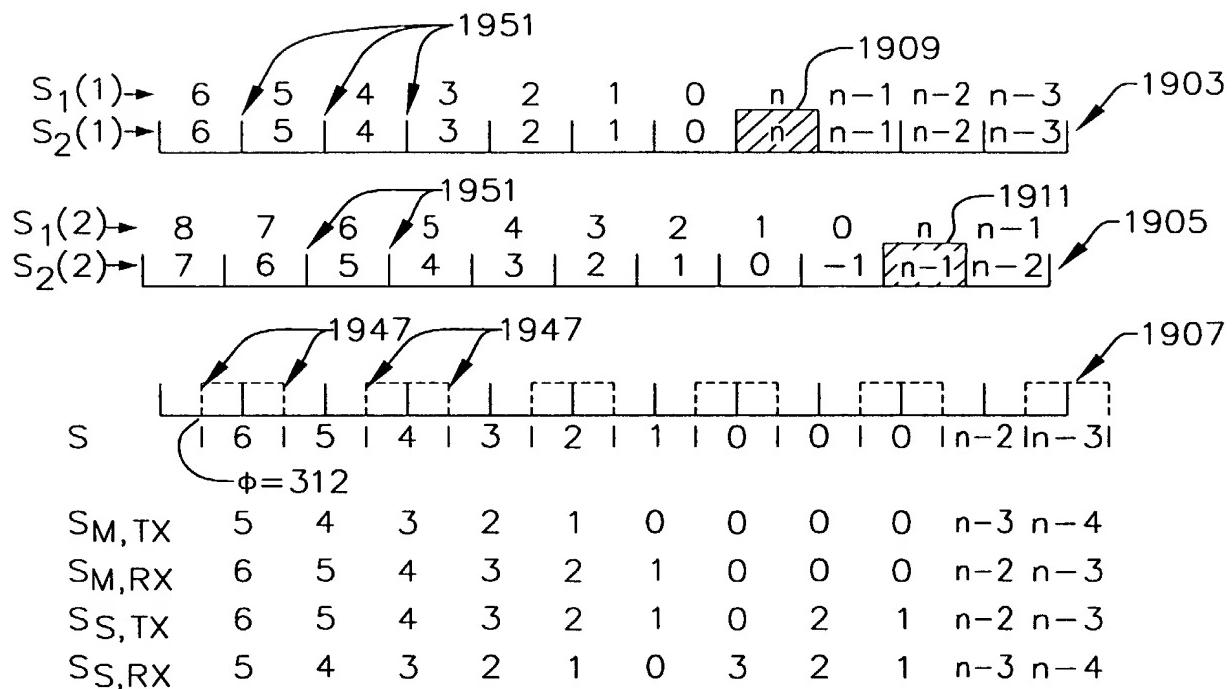


FIG. 19B

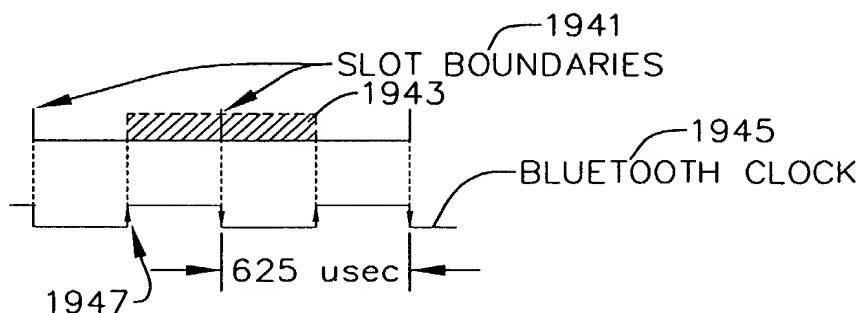


FIG. 20

TABLE 1. PACKET TYPE PRIORITY → 2001

RANGE	MIN	MAX	1ST.	2ND.	3RD.	4TH.	5TH.	6TH.	CHOICE	CHOICE	CHOICE	CHOICE
LABEL	BYTES IN	BUFFER										
a	0	0	NULL	NULL								
b	1	17	DM1	DH1	DM3	DH3	DM5	DH5	DM5	DH5	DM1	DM1
c	18	27	DH1	DM3	DH3	DM5	DH5	DH5	DM5	DH5	DM1	DM1
d	28	121	DM3	DH3	DM5	DH5	DM1	DH1	DM1	DH1	DM1	DM1
e	122	183	DH3	DM5	DH5	DM3	DH3	DH1	DM3	DH1	DM1	DM1
f	184	224	DM5	DH5	DH3	DM3	DH1	DM1	DM3	DH1	DM1	DM1
g	225	339	DH5	DM5	DH3	DM3	DH3	DH1	DM3	DH1	DM1	DM1
h	339	DH5	DM5	DH3	DM3	DH3	DM3	DH1	DM3	DH1	DM1	DM1

FIG. 21

EXAMPLE OF A FRAGMENT CHOOSEN FOR 16 FRAGMENTS, N=4
 (FOR 127 FRAGMENTS N=7)

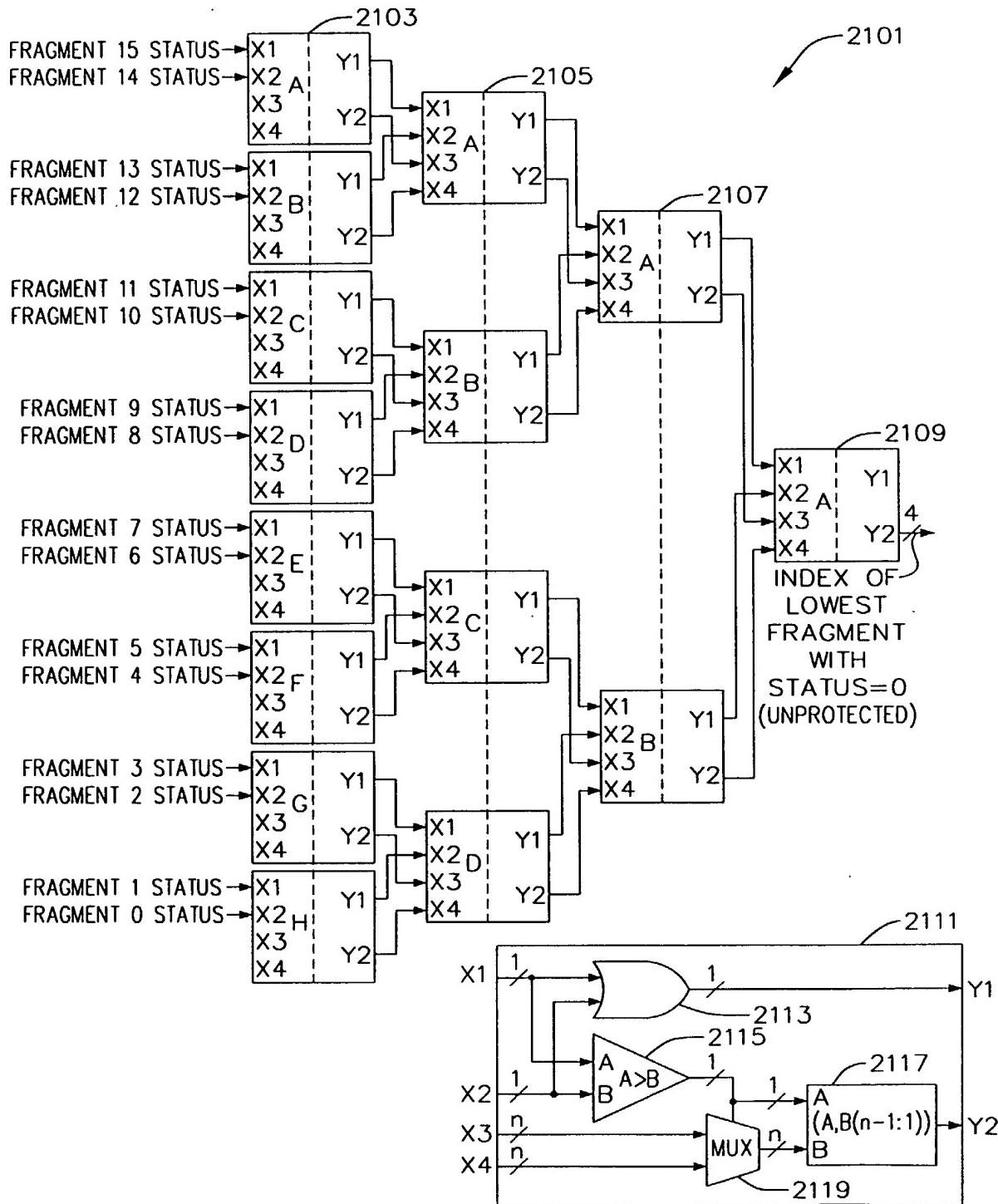


FIG.22

CIRCUIT TO CALCULATE CLK MOD Y, WHERE CLK IS 27 BITS AND T IS 8 BITS

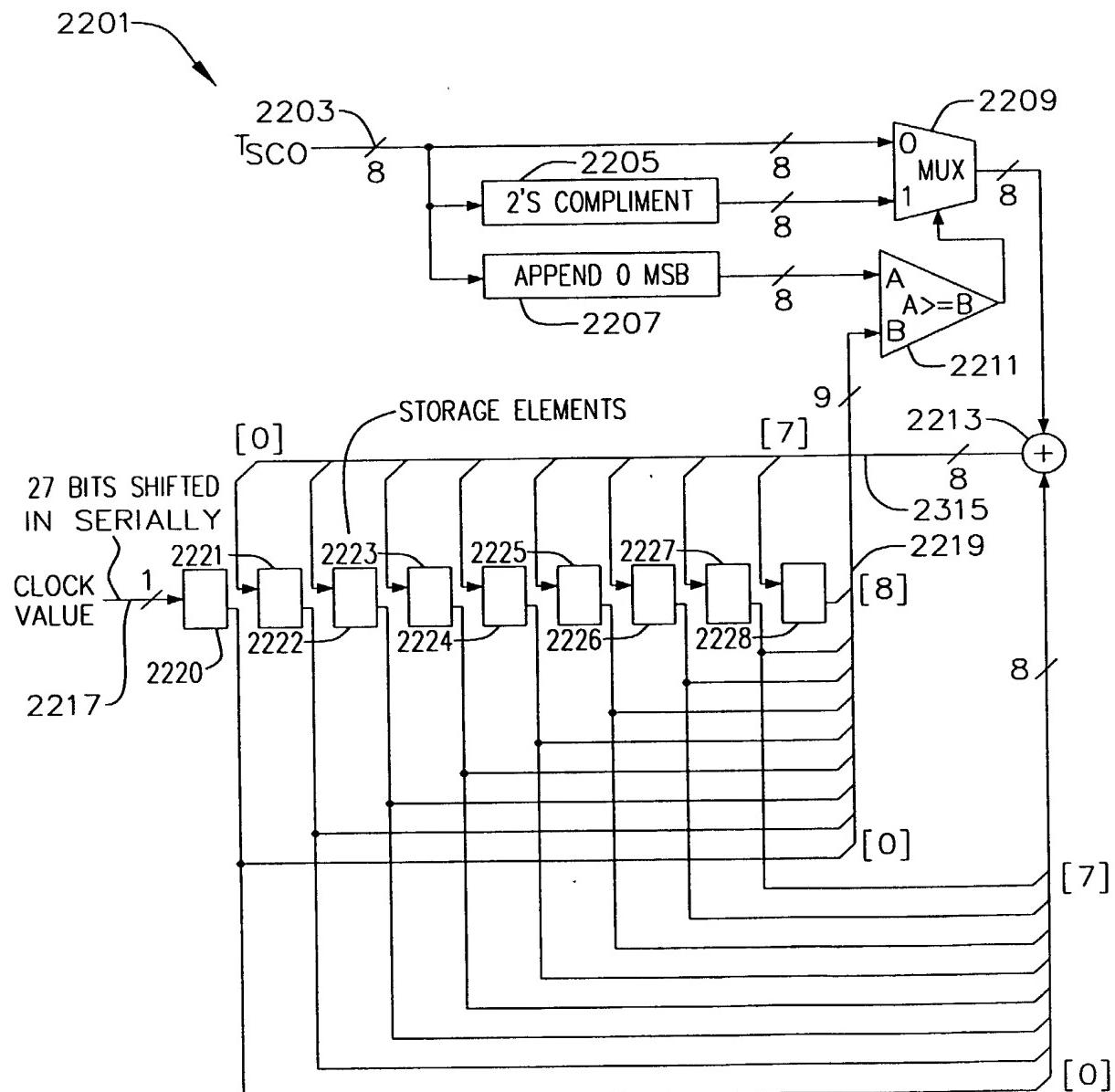


FIG.23

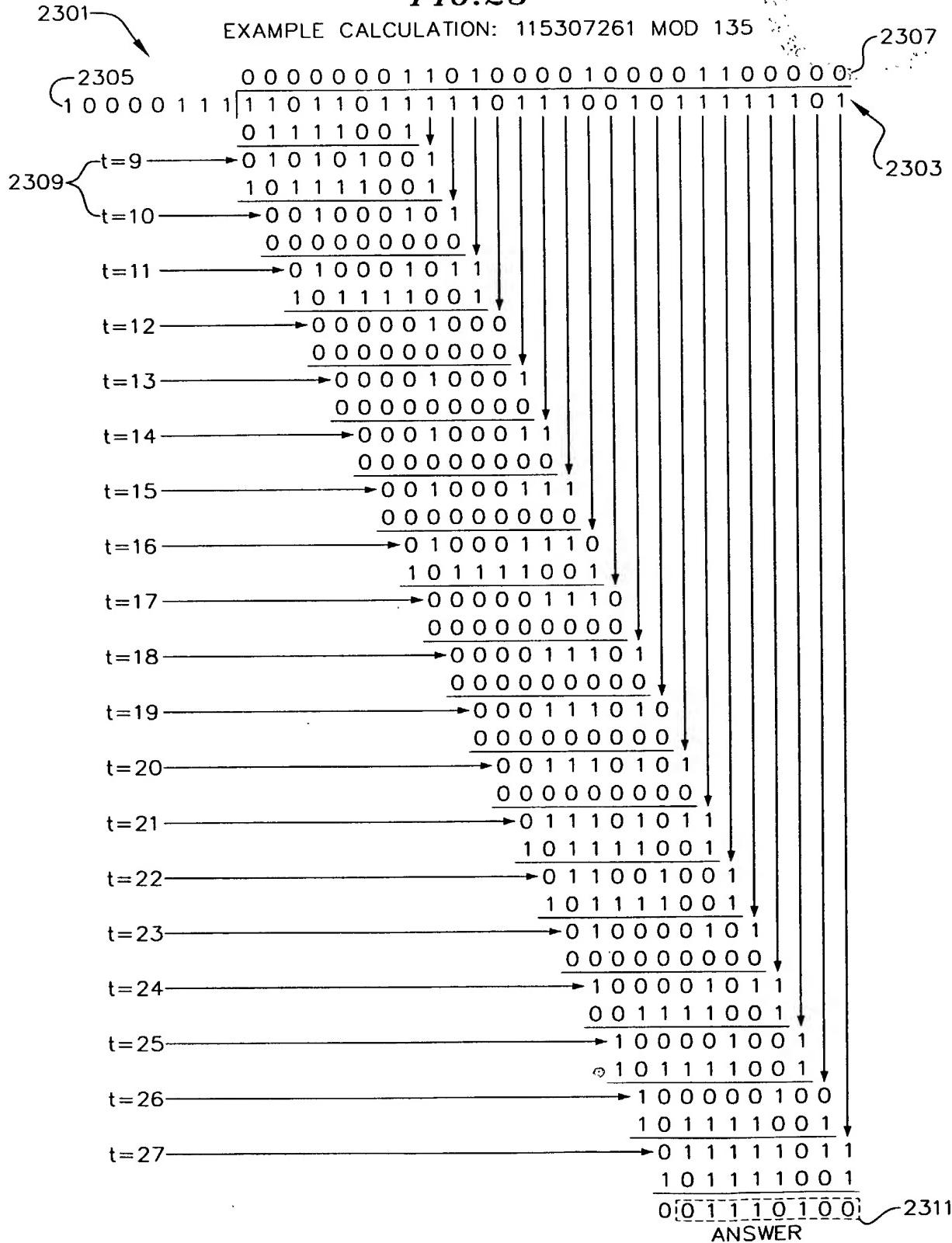


FIG.24

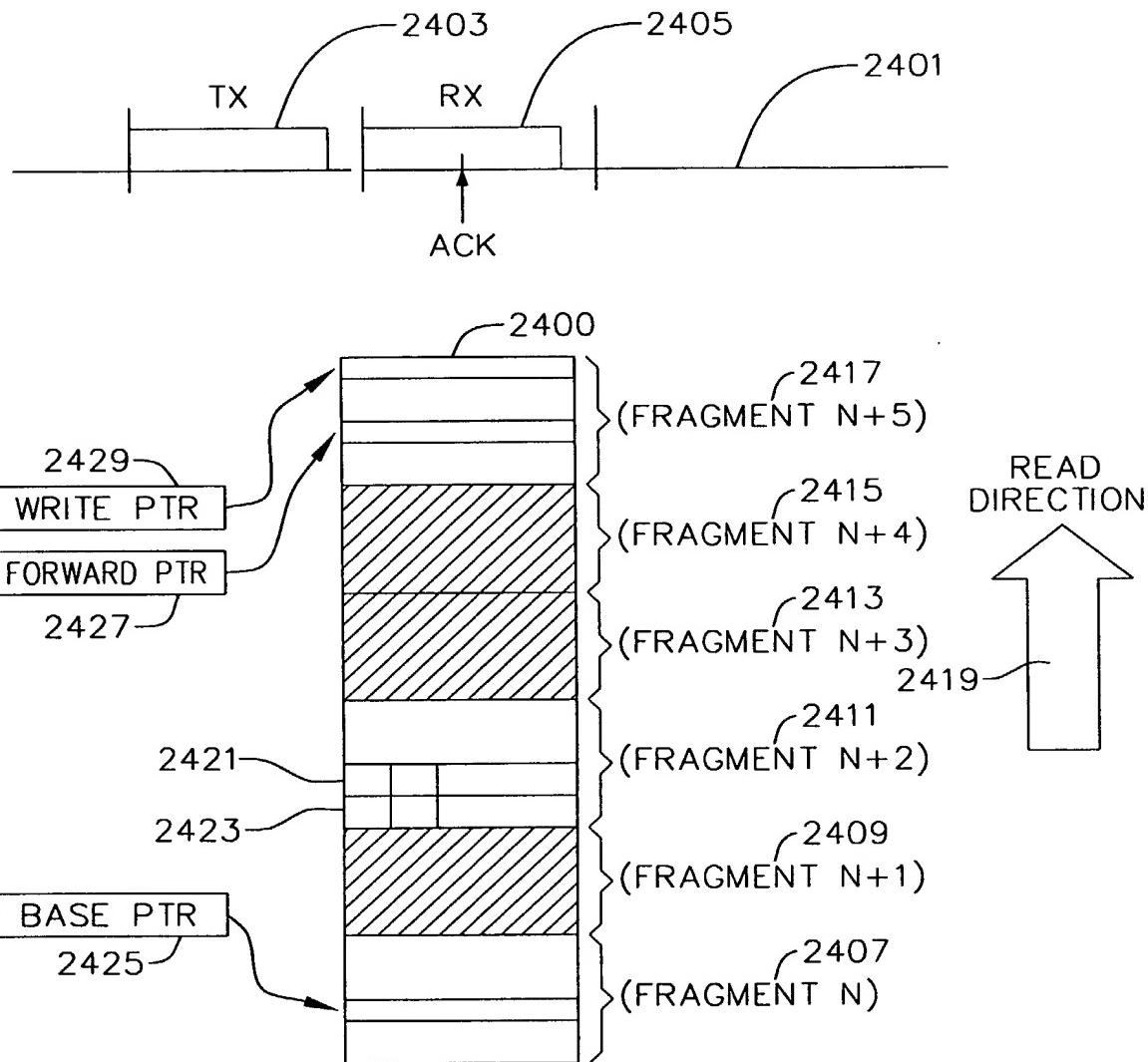


FIG.25

THE FOLLOWING DIPICTS THE BYTE GAUGE STATE MACHINE IN FLOW DIAGRAM FORM. NUMBERS IN PARENTHESES INDICATE THE STATE.

$\text{ACL}(X)$: DATA IN ACL RAM
 ADDRESS LOCATION X
 ACC : BYTE COUNT ACCUMULATOR
 BPTR : ACL BASE READ
 ADDRESS POINTER
 DCNT : BASE FRAGMENT BYTE
 DOWN-COUNT
 FF : "FIRST FRAGMENT" BIT
 FLEN : FRAGMENT LENGTH IN BYTES
 LPTR : LINK POINTER FOR
 FRAGMENT $\text{TPTR}(12:6)$
 L_CH : L2CAP L_CH FIELD
 LF : "LAST FRAGMENT" BIT
 TPTR : TEMPORARY ACL RAM
 ADDRESS POINTER

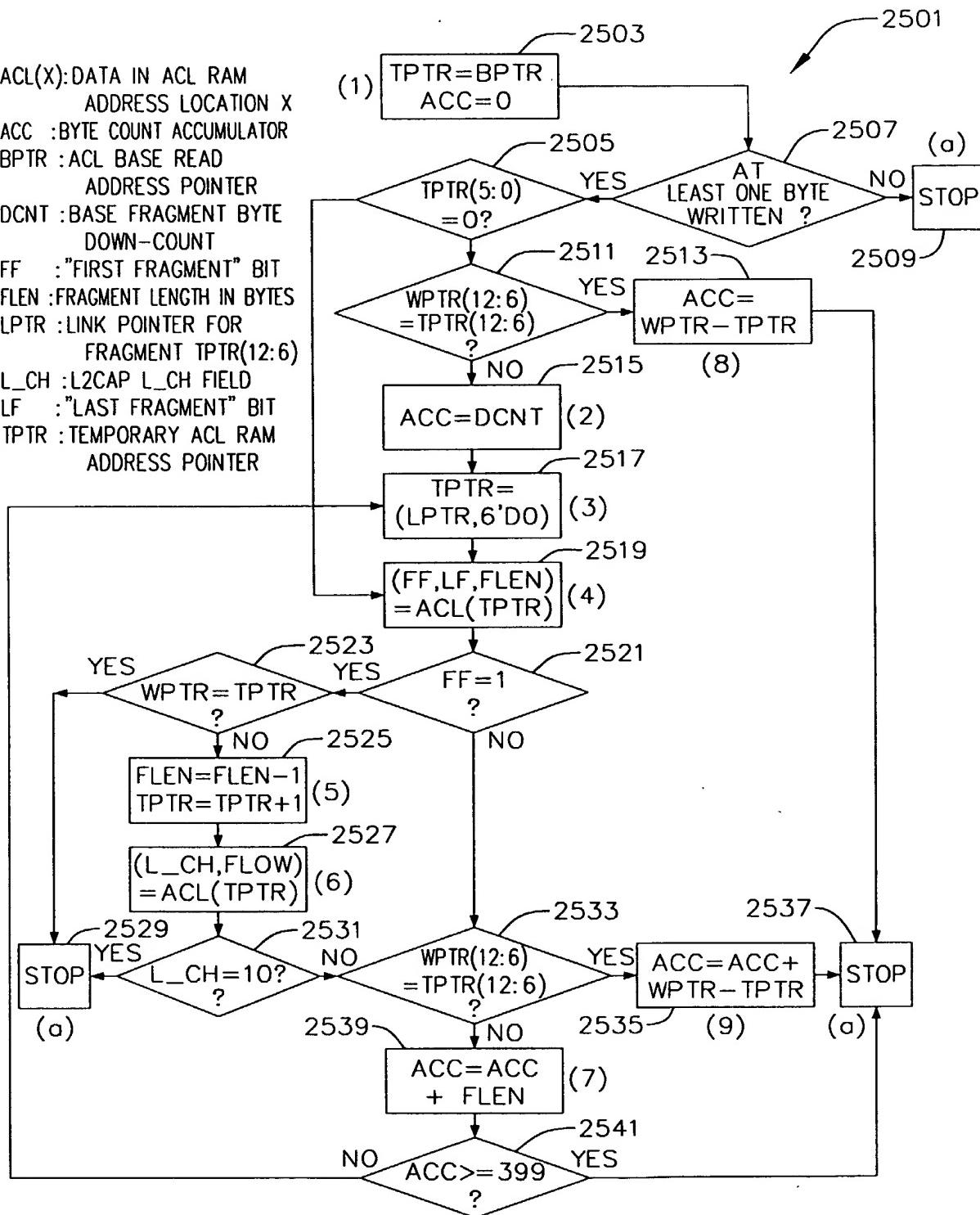


FIG. 26
L2CAP PACKET FLUSH STATE MACHINE

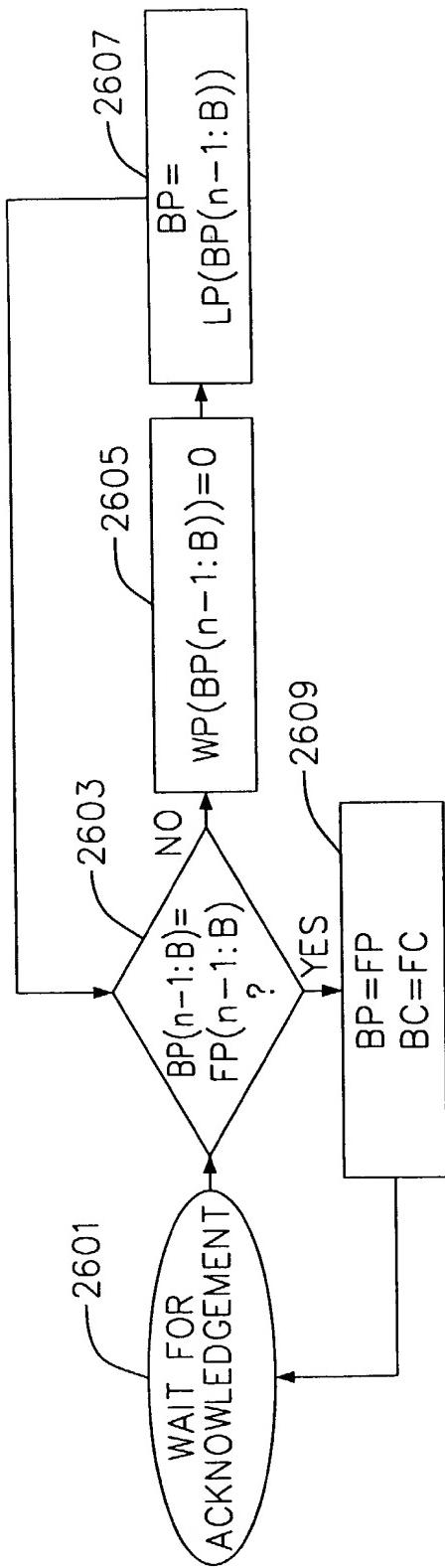


FIG. 27

